

APR 03 2007

Application No.: 10/701,261

Docket No.: JCLA7806-R

AMENDMENT

In The Claims:

Please amend the claims as follows:

Claims 1 (previously presented) A control chip built inside an integrated circuit for reducing electromagnetic interference, wherein the control chip is able to spread out the frequency of an electromagnetic interference signal according to an algorithm, wherein the control chip picks up the algorithm from an external bus, and

the electromagnetic interference signal at each frequency are modulated according to a corresponding spread out width.

Claim 2 (canceled)

Claim 3 (previously presented) A control chip for reducing electromagnetic interference, comprising:

a software phase lock loop built inside the control chip for receiving a clock signal and spreading out the frequency of an electromagnetic interference signal according to an algorithm received from an external bus, wherein the electromagnetic interference signal at each frequency are modulated according to a corresponding spread out width; and

a bus coupled to the software phase lock loop for inputting the algorithm.

Application No.: 10/701,261

Docket No.: JCLA7806-R

Claim 4 (original) The control chip of claim 3, wherein the frequency of the electromagnetic interference signal and the spread out width at that frequency is set by the algorithm within the software phase lock loop.

Claim 5 (currently amended) An application specific integrated circuit for reducing electromagnetic interference, comprising:

- a first input terminal for receiving a clock signal;
- a second input terminal for receiving an algorithm from an external bus; and
- a software phase lock loop coupled to the first input terminal and the second input terminal for spreading out the frequency of an electromagnetic interference signal according to the clock signal and the algorithm, wherein the electromagnetic interference signal at each frequency are modulated according to a corresponding spread out width.

Claim 6 (original) The application specific integrated circuit of claim 5, wherein the frequency of the electromagnetic interference signal and the required spread out width at that frequency are set by the algorithm within the software phase lock loop.

Claim 7 (previously presented) A method of reducing the strength of an electromagnetic interference signal, comprising the steps of:

- receiving an algorithm from an external bus;
- determining a specified frequency of the electromagnetic interference signal and a

Application No.: 10/701,261

Docket No.: JCLA7806-R

corresponding spread out width at that frequency according to the algorithm; and

spreading out the electromagnetic interference signal according to the spread out width using the specified frequency as the center of spreading.